

CALL FOR APPLICATIONS

2026 FALL HANDS-ON WORKSHOP ON CHIP DESIGN AND FABRICATION



1–2 October 2026



Panama City,
Panama

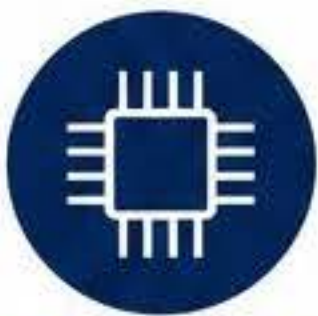
*From ideas
to impact*

Join an immersive, hands-on workshop where you will design, fabricate, and test your own **application-specific integrated circuits (ASIC)**, taking them all the way through to production – with the goal of holding a fully-packaged and ready-to-use chip in your hand at the end of the process.



SCHOLARSHIPS AVAILABLE

For IEEE professionals or students from Latin America with an advanced level of microelectronics.



HANDS-ON EXPERIENCE

Design and build
your own ASIC



EXPERT GUIDANCE

Learn from industry
and academic experts



REGIONAL OPPORTUNITY

Connect with the IEEE
community in
Latin America



REAL-WORLD IMPACT

Gain practical skills
for the future of
semiconductors

APPLY NOW



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APPLICATION DEADLINE
14 SEPTEMBER 2026



Questions? Contact us at
laedc@ieee.org

